

World's largest and fastest FIFO Memory Buffer for 4K2K UltraHD Video Applications

The AL462 First-In-First-Out (FIFO) memory consists of 512Mbits memory density and can be configured as a 16M by 32-bit or dual 16-bit bus interface and operated at maximum R/W speeds up to 150 MHz for 4K2K video frame buffer; equivalent to 9.6GB/sec of simultaneous read/write data throughput.

AL462 FIFO provides easy-to-use I/O interfaces and controls, such as selectable control signal polarity, two frame buffer modes, facilitating usage in multimedia systems, video capture systems and various video data buffering applications. Manufactured using state-of-the-art technologies and available as a 249-ball LFBGA package, allow product designers to keep power consumption and real estate to a minimum.

AL462 FIFO Advantages

- Extremely simple design interface
- Supports HD video application up to 4K2K UltraHD resolution
- Reduces design/development efforts
- Reduces overall costs
- Faster time to market
- Improves system performance
- Easy retrofit for existing designs

Applications



HD video cameras



Video/Audio Broadcasting



Video Surveillance



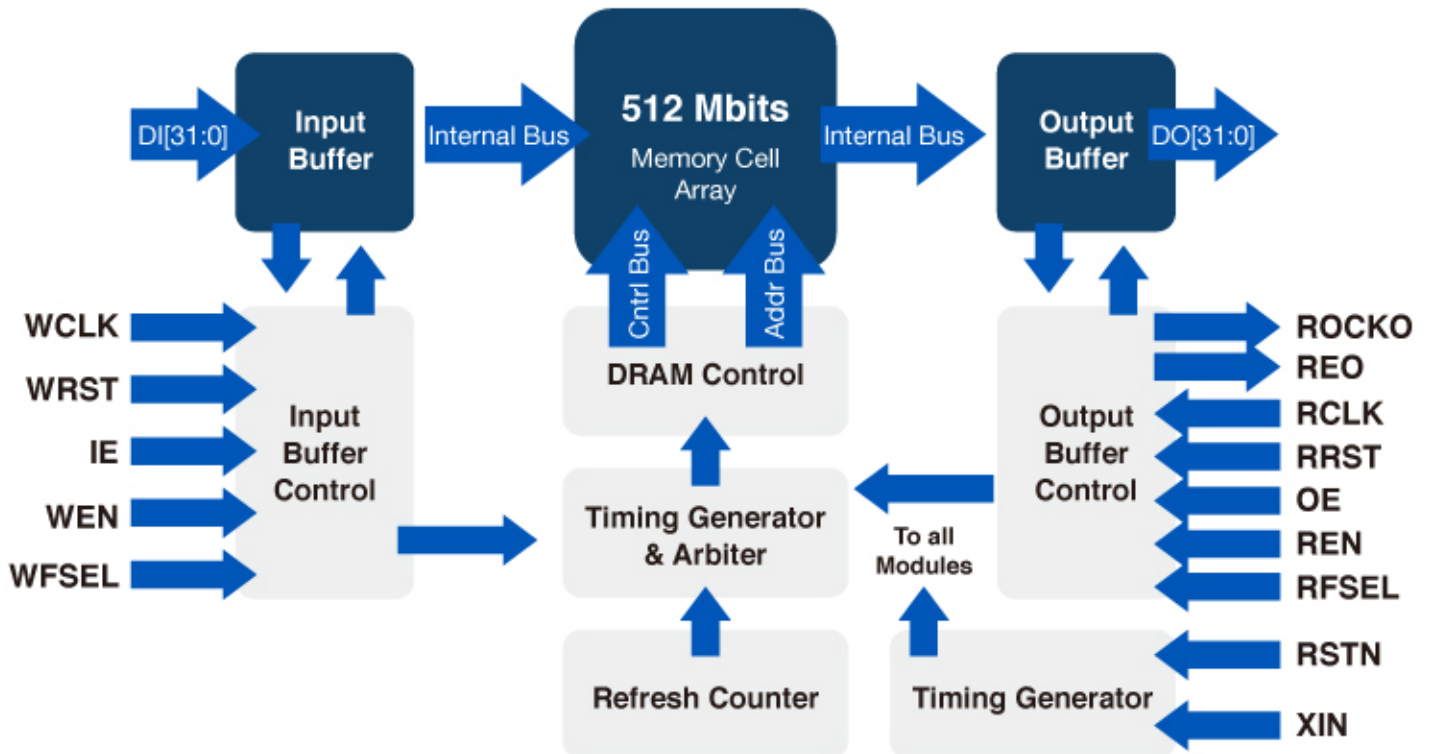
Medical Systems

Industrial Instruments and many more...

Features

- 512M-bits configurable to 32-bit or 16-bit x2 bus width FIFO (First-In-First-Out) memory
- Independent input/output synchronous data access (different I/O data rates acceptable)
- Supports one 16Mx32-bit or dual 16Mx16-bit organizations
- High speed synchronous sequential access in Maximum 150Mhz clock rate
- 9.6 GB/sec simultaneous R/W throughput
- Supports input/output enable controls
- Selectable control signal polarity
- Supports output timing sync clock and controls
- Supports 2-frame mode for double buffering
- 1.8V/3.3V power supply
- Standard 249-ball LFBGA package

Block Diagram



Contact Details

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