

LT89101L --- Product Brief

MIPI-to-LVDS Level Shifter

Features

- Support 3-bit maximum 10dB Input Equalization
- Signal Re-driving without Data De-packetize
- Input Data Lane and Polarity Swapping
- 80Mb/s~2.5Gb/s per Data Lane for Both Input and Output
- 1.8V Single Supply Power
- Support 100KHz / 400KHz I2C slave
- External 25MHz Crystal Reference Clock
- Temperature Range: -40°C ~ +85°C
- Packaged in QFN64 7.5mm x 7.5mm and BGA81 5mm x 5mm

Description

The Lontium LT89101L is a high performance MIPI-to-LVDS

level shifter for different system application.

The LT89101L is fabricated in advanced CMOS process and implemented in a small outline 7.5mm x 7.5mm QFN64 package. This package is RoHS compliant and specified to operate from -40°C to +85°C.

Applications

- Mobile systems
- Home security systems
- Digital video cameras
- Digital still cameras
- Tablet PC, Notebook PC
- Car Display and Camera System
- Signal bridge in FPGA verification

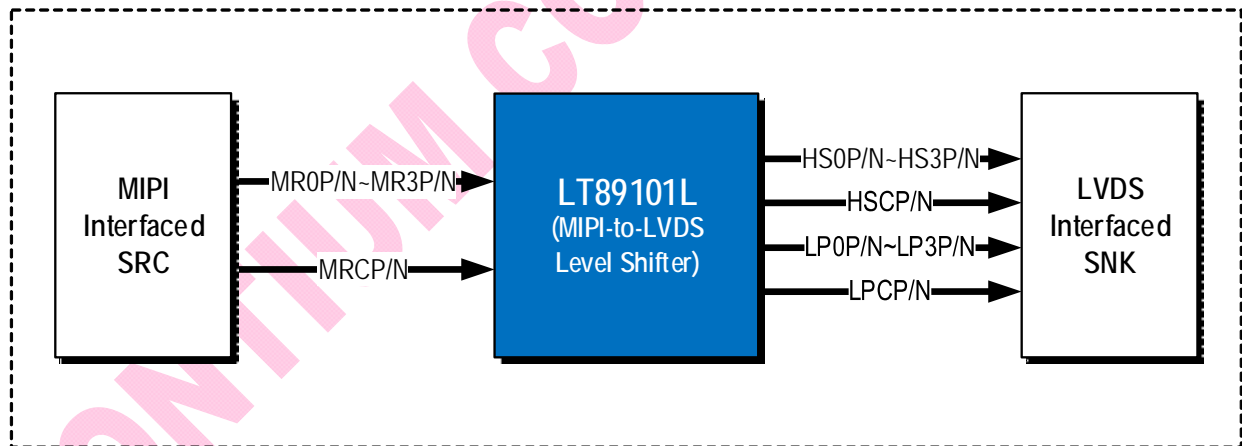


Figure 1. Typical Application and System Diagram

Ordering Information

Part Number	Operating Temperature Range	Package	Packing Method
LT89101L	-40° C to +85° C	QFN64 (7.5*7.5)/BGA81 (5*5)	Tray

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Technical support: support@lontium.com

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